



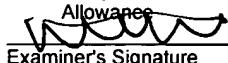
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APPLICANTS					
Hiroyoshi Kuge, Kanagawa, JAPAN; Yoshihiro Ohara, Kanagawa, JAPAN; ** CONTINUING DATA ***** ** FOREIGN APPLICATIONS ***** JAPAN 2003-051469 02/27/2003					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 05/19/2004					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged  Examiner's Signature		STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 10	INDEPENDENT CLAIMS 4
ADDRESS 000466 YOUNG & THOMPSON 745 SOUTH 23RD STREET 2ND FLOOR ARLINGTON, VA 22202					
TITLE Semiconductor integrated circuit device and design automation apparatus, method and program					
FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)		
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